To whom it may concern,

I am writing this recommendation letter for my former supervised student Jiajun Hu.

I got to know and realize Jiajun as the module convenor of EEEE 2059 (project-based learning embedded electronics project) and EEEE3102 (Analogue Electronics). I am now supervising his final year graduation project and thesis which mainly focuses on FPGA neural network hardware acceleration.

Jiajun had achieved excellent academic performance and showed great hands-on project building ability in these modules as the records in transcripts. At the same time, Jiajun had showed outstanding leadership and team spirits by collaborating with teammates solving the projects issues. During the project construction period, students were required to accomplish the speed detection function using a specified ultrasonic radar module and STM32L based on doppler effect. Students will acquire basic hardware debugging skills and hands-on electronic circuit experience. It also improved the C/C++ programming skills when coding Discrete Fourier Transform and signal processing algorithms on STM32L. Jiajun and his team had conquered the pandemic effects, achieving a fine and complete system function within a shortened semester and project duration by COVID-19.

Jiajun is also such a motivative and inspiring person. During the COVID-19 pandemic and winter vacation, Jiajun had self-learned Python programming and some basic data processing skills, which was of essential significance in his later summer research project. During the quarantine period, Jiajun had maintained his consistency and discipline to study further and wider not only including class material but also contains lots of extra resources through me and internets. His productivity and motivation also helped him perform well in the later 2-month summer research project, who was aiming at acoustic speech processing using Python.

In the last summer vacation, Jiajun was continuing his great learning ability and work consistency with wild research and deep understanding for various signal processing algorithms and topics including open-source code repositories, published papers and some classic textbooks. Jiajun was always recording his discovers and issues on daily basis using LaTeX and Markdown notebook through a shared GitHub repository which informed me of the whole project progress regularly and clearly.

At this moment, Jiajun is following up his summer project for his bachelor graduation project focusing on Machine Learning Hardware Acceleration using Xilinx FPGA board and High-Level-Synthesis under my supervision. I believe that the consistent discipline and ambitious motivation will favor Jiajun in his later graduate study and further career path.